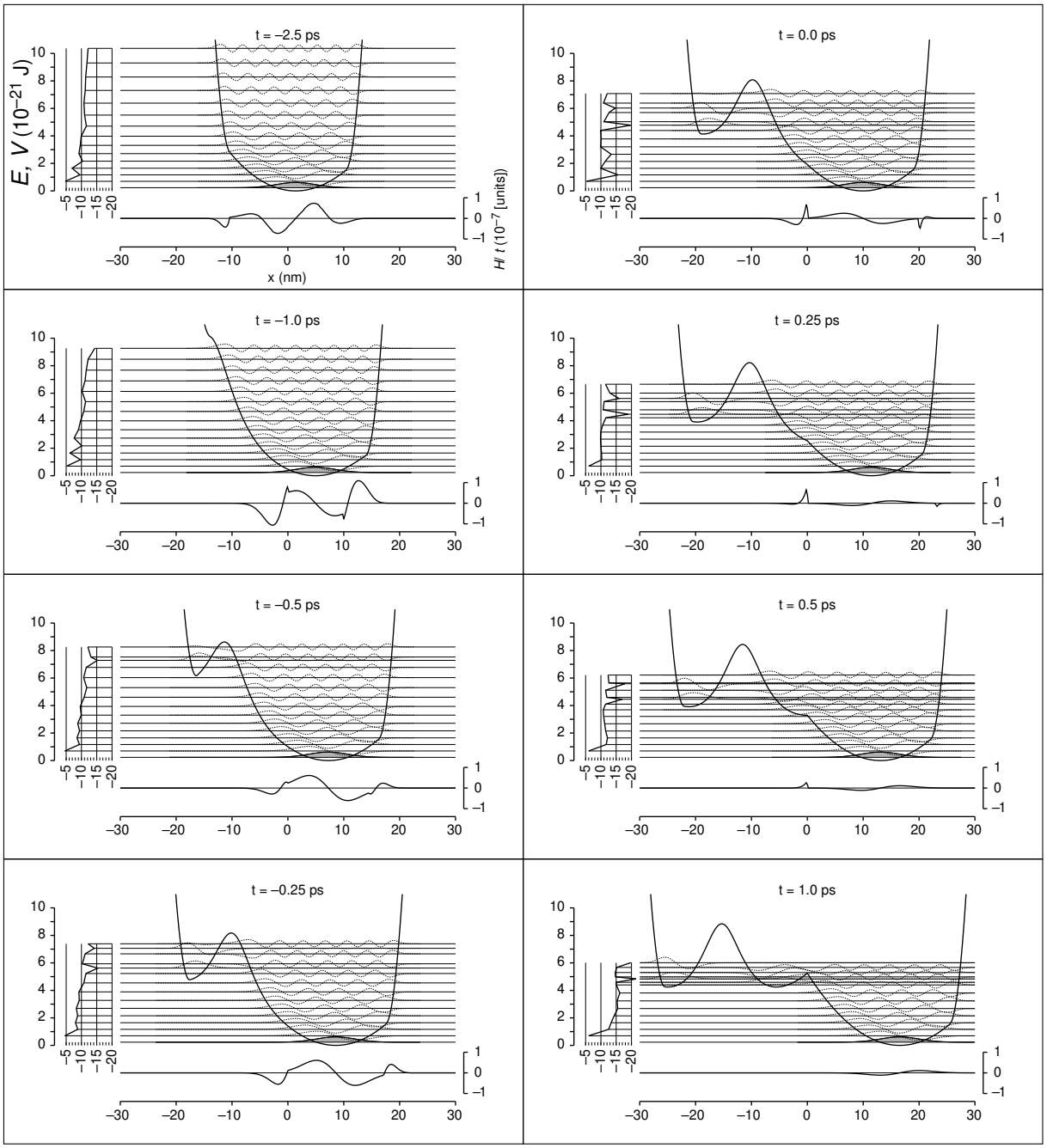

Figure 8. Plots of the magnitude of the first several excited states of the switched charge during a switching operation.

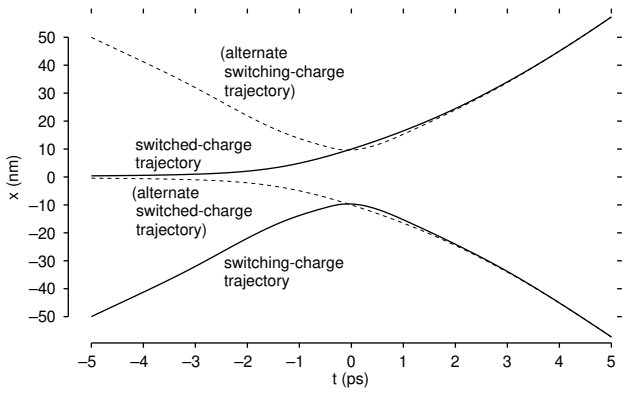

Figure 9. Plots of $x_{switching}$ and $x_{switched}$ as functions of the switching time in picoseconds. Dotted curves illustrate the paths that would be followed if the switching charge occupied the channel representing the alternate logic state.

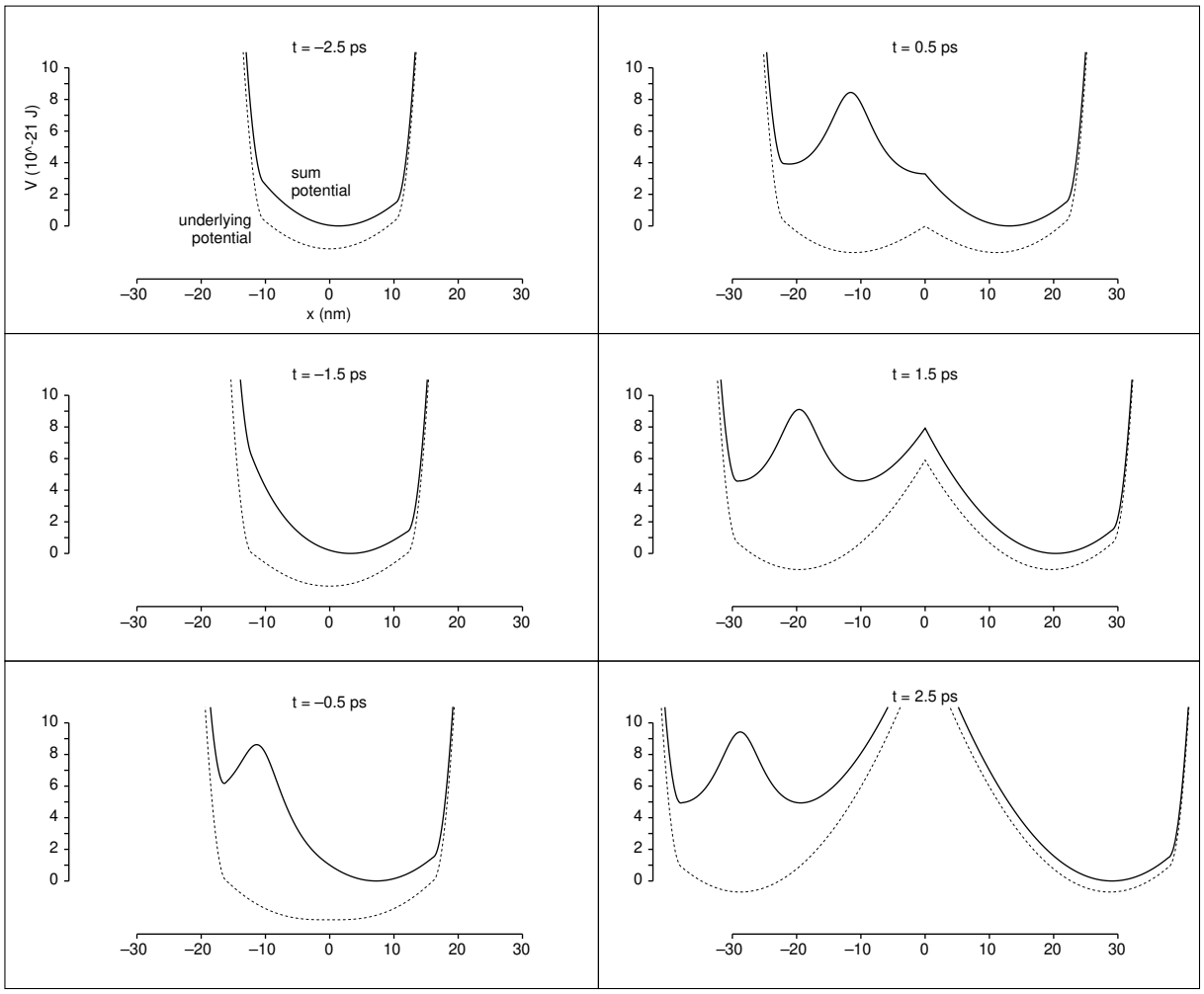

Figure 10. Plots of V_{net} and V_{net} - V_{elect} (the underlying potential) at a series of z coordinates, and corresponding t coordinates for $dz/dt = 2 \times 10^{11}$ sec^{-1} (that is, t_{char} = 5 ps).